



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

In re the Application of:

Pendharkar et al.

Atty Docket: **TI-33654**

Serial No.: **10/678,028**

Art Unit: **TBD**

Filed: **09/29/2003**

Examiner: **TBD**

Title: **A LOW NOISE VERTICAL VARIABLE GATE CONTROL VOLTAGE
JFET DEVICE IN A BICMOS PROCESS AND METHODS TO BUILD THIS
DEVICE**

LETTER TO OFFICIAL DRAFTSPERSON

M. S. Missing Parts
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING UNDER 37 CFR 1.8(a)

I hereby certify that this document is being deposited with the
United States Postal Service with sufficient postage for First
Class Mail in an envelope addressed to: Commissioner for
Patents, Alexandria VA 22313-1450:

1-7-04
Jackie McBride

Jackie McBride

Dear Sir:

In response to the Notice to File Corrected Application Papers – Filing Date
Granted, please find enclosed three (3) sheets of formal drawings. A copy of the Notice
is also enclosed.

Texas Instruments Incorporated
P. O. Box 655474 MS 3999
Dallas, TX 75265
(972) 917-5355

Respectfully submitted,

Yingsheng Tung

Yingsheng Tung
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UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING OR 371 (c) DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/678,028	09/29/2003	Sameer P. Pendharker	TI-33654

23494
 TEXAS INSTRUMENTS INCORPORATED
 P O BOX 655474, M/S 3999
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CONFIRMATION NO. 9934

FORMALITIES LETTER



OC000000011547815

Date Mailed: 12/22/2003

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

An application number and filing date have been accorded to this application. The application is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given TWO MONTHS from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

The required item(s) identified below must be timely submitted to avoid abandonment:

- Replacement drawings in compliance with 37 CFR 1.84 and 37 CFR 1.121 are required. The drawings submitted are not acceptable because:
 - The drawings must be reasonably free from erasures and must be free from alterations, overwriting, interlineations, folds, and copy marks. See Figure(s) 1a, 1b, 1c.

Replies should be mailed to: Mail Stop Missing Parts
 Commissioner for Patents
 P.O. Box 1450
 Alexandria VA 22313-1450

*A copy of this notice **MUST** be returned with the reply.*

T.TU
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 Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE